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S38	14	plug-in with chipset	US-PGPUB; USPAT	OR	ON	2004/12/12 19:42
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*Papadopoulos, P.M.; Katz, M.J.; Bruno, G.;*

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#### 2 Using multirail networks in high-performance clusters

*Coll, S.; Frachtenberg, E.; Petrini, F.; Hoisie, A.; Gurvits, L.;*

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#### 3 Performance and architecture of SGSN and GGSN of general packet radio service (GPRS)

*Mishra, A.;*

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#### 5 A Web-based TelePACS using an asymmetric satellite system

*Seon-Cheol Hwang; Myoung-Ho Lee;*

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**8 A shared memory model on a cluster of PCs**

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*Sarjoughian, H.S.; Zeigler, B.P.; Hall, S.B.;*

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Pages:201 - 213

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#### 2 Reusable multimedia content in Web based learning systems

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*Gong, L.;*

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*Marsic, I.;*

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*Wills, L.; Kannan, S.; Sander, S.; Guler, M.; Heck, B.; Prasad, J.V.R.; Schrage, D.; Vachtsevanos, G.;*

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**12 Design lessons for building agile manufacturing systems**

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**16 Web development: estimating quick-to-market software**

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*Kerridge, J.; Welch, P.; Wood, D.;*

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**19 Integrative systems: assessing requirements and capabilities for intra- and inter-organizational contexts**

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 structures based on busses. Examples are the **PCI bus** and Ethernet network. However, busses have  
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 be connected to a Linux host system through the **PCI bus**. The bus interface will be implemented as an  
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 between the InfiniBand network and the **PCI bus**. We could easily simulate many scenarios  
**Object-Oriented** High-Level Modeling of an InfiniBand to  
[www.research.ibm.com/pics/verification/ps/hlm\\_fdl02.pdf](http://www.research.ibm.com/pics/verification/ps/hlm_fdl02.pdf)

[Formal Specification of the Virtual Component Interface.. - Bunker, Gopalakrishnan \(2001\) \(Correct\)](#)  
 Section 4 briefly describes our previous VCI-to-**PCI bus** wrapper verification case study and the lessons  
 operational semantics for UML Use Cases in an **object-oriented** specification language named Odal. Again,  
[www.cs.utah.edu/~abunker/pubs/uucs-01-007.ps](http://www.cs.utah.edu/~abunker/pubs/uucs-01-007.ps)

[Embedded Programming with C++ - Stephen Williams Picture \(Correct\)](#)  
 support package a console driver that uses the **PCI bus** to communicate as a console. The MON960 monitor  
 Proceedings of the Third USENIX Conference on **Object-Oriented** Technologies and Systems Portland, Oregon,  
[www.sage.usenix.org/publications/library/proceedings/coots97/full\\_papers/williams/williams.ps](http://www.sage.usenix.org/publications/library/proceedings/coots97/full_papers/williams/williams.ps)

[Collaborative Efforts To Support System-Level Design Education - Bouldin \(Correct\)](#)  
 ARM, MIPS and Sparc#MPEG decompression engines, **PCI bus** controllers, specialized DSPs, etc. Combining  
 #Support for heterogeneity through **object-oriented** encapsulation, Emphasis on veri#cation  
[microsys6.engr.utk.edu/ece/collab99.pdf](http://microsys6.engr.utk.edu/ece/collab99.pdf)

A programming environment to control switching.. - Legrand.. (1996) (Correct)  
transparently. Interfacing this chip with a **PCI-bus** by using fast FIFOs for matching the speed  
in Java(tm) Sun Microsystems) Java is an **object oriented** language which is platform independent  
[www.ifh.de/~legrand/Papers/aihenp96.ps.gz](http://www.ifh.de/~legrand/Papers/aihenp96.ps.gz)

The C// Data Parallel Language on a Shared Memory Multiprocessor - Fatni, Houzet (Correct)  
efficient communication mechanism. Features in the **PCI bus** and the network module are combined to provide a  
as a shared-memory machine. Many parallel **object oriented** implementations based on C/Chave been  
[www.enseeiht.fr/Recherche/Info/Archit/CAMPB97.ps](http://www.enseeiht.fr/Recherche/Info/Archit/CAMPB97.ps)

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